



# UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
-----------------	-------------	----------------------	---------------------	------------------

10/705,385

11/10/2003

Pierre Liu

2011136

4322

7590

11/17/2004

Keith Kline  
PRO-TECHTOR INTERNATIONAL  
20775 Norada Court  
Saratoga, CA 95070-3018

EXAMINER

GEBREMARIAM, SAMUEL A

ART UNIT

PAPER NUMBER

2811

DATE MAILED: 11/17/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

Application No.

10/705,385

Applicant(s)

LIU, PIERRE

Examiner

Samuel A Gebremariam

Art Unit

2811

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☐ Responsive to communication(s) filed on \_\_\_\_.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-7 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-7 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. ____. |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date ____. | 6) <input type="checkbox"/> Other: ____.  |

## **DETAILED ACTION**

### ***Claim Objections***

1. Claim 3 and 4 recite the limitation "the glue layer ". There is insufficient antecedent basis for this limitation in the claim.

The limitation "is used to arrange in the electric device" as recited in claim 6, line 9 appears to have a typographical error. Appropriate correction is required.

### ***Specification***

2. The disclosure is objected to because of the following informalities: 35 U.S.C. 112, first paragraph, requires the specification to be written in "full, clear, concise, and exact terms." The specification is replete with terms which are not clear, concise and exact. The specification should be revised carefully in order to comply with 35 U.S.C. 112, first paragraph. Examples of some unclear, inexact or verbose terms used in the specification are: "gingers", "the memory card may be recognized the aspect of the golden fingers". Appropriate change is required.

### ***Claim Rejections - 35 USC § 103***

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 1-7 are rejected under 35 U.S.C. 103(a) as being unpatentable over admitted prior art (APA) in view of Horio et al. US patent No. 6,590,152.

Art Unit: 2811

Regarding claim 1, APA teaches (fig. 1) a small memory card, comprising a substrate (10) having an upper surface and a lower surface, the upper surface formed with a plurality of connected points (region where memory chip is formed also refer to the description of the prior art) and a plurality of golden fingers (12) electrically connected to the plurality of connected fingers, the substrate arranged in the electric device, so that the plurality of golden fingers may be electrically connected to the electric device (page 1 of the specification, lines 8-14); and at least one memory chip (16), which is arranged on the upper surface of the substrate, electrically connected to a plurality of connected point.

APA does not teach a monitor to be set in an electric device and at least one monitor arranged on the side of the upper surface of the substrate, so that the aspect and location of the plurality of golden fingers may be recognized.

Horio teaches (fig. 4) the formation an electronic device (2C) in conjunction with a monitor (2A) and 2B that is arranged on the side of the upper surface of the substrate (10).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to incorporate the monitor structure taught Horio in the device of APA in order to provide an infrared data communication means.

Regarding claim 2, APA teaches substantially the entire claimed structure of claim 1 above including the monitor is an LED (2A) (Horio fig. 4).

The combined structure of APA and Horio results in the LED that is arranged on the side of the plurality of golden fingers of the substrate.

Art Unit: 2811

Regarding claim 3, APA teaches substantially the entire claimed structure of claim 1 above including a glue layer (11, Horio) that is transparent.

Regarding claim 4, APA teaches substantially the entire claimed structure of claim 1 above including the monitor (2A and 2B) is revealed out of the glue layer (11) (the monitors 2A and 2B can be seen, because layer (11) is transparent.

Regarding claim 5, APA teaches substantially the entire claimed structure of claim 1 above including the memory chip (16) is electrically connected to the plurality of connected points of the substrate by a plurality of wires (refer to page 1 of the specification, lines 8-14, APA).

Regarding claims 6 and 7, APA teaches substantially the entire claimed structure of claims 1 and 2 above including at least one memory chip (16) where the monitor is arranged on the side of the upper surface of the substrate, so the aspect and location of the plurality of golden fingers may be recognized.

### ***Conclusion***


Any inquiry concerning this communication or earlier communications from the examiner should be directed to Samuel A Gebremariam whose telephone number is (571) 272-1653. The examiner can normally be reached on 8:00am-4:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie Lee can be reached on (571) 272-1732. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Art Unit: 2811

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

SAG  
November 12, 2004



**EDDIE LEE**  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2800